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XILINX, INC

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(Depositor's name)	Pat Tompkins
(Signature)	tot Tom Daims
(Date)	August 8 2006
	August 8, 2006

204 254 550 470	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
APPLICATION NO. FILING DATE		34 1000 110	2232	
10/086 179	02/28/2002	David P. Schultz	X-1069 US	ZZJZ

10/086,129 TITLE OF INVENTION: METHOD AND SYSTEM FOR FLEXIBLY NESTING ITAG TAP CONTROLLERS FOR FPGA-BASED SYSTEM-ON-CHIP (SQC)

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete submitting the completed applications for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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MAJL STOP ISSUE FEE MPGOMMISSIONER FOR PATENTS P. Q. Box 1450 Alekandria, Virginia \$22393-1450 David P. Schultz Inventor(s):

Assignee:

XILINX, INC.

10/086,129

conf.no. 2232

Serial No.: Filed:

February 28, 2002

Title: Method and System for Flexibly Nesting JTAG TAP Controllers for FPGA-Based

System-On-Chip (SoC)

Docket No.: X-1069 US

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- Fee Transmittal - Part B

Date: August 8, 2006

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